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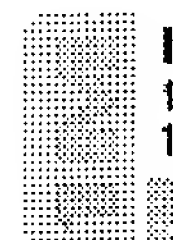
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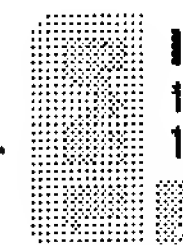
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1   **High-speed bus debug and validation test challenges**
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Pages:218 - 222

[Abstract]   [PDF Full-Text (615 KB)]   **IEEE CNF**

2   **A multigigahertz clocking scheme for the Pentium(R) 4 microproce**
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